



Ragh Kuttappa

Clock Synchronization for Multi-die Systems

Wednesday, January 15th
Bossone 302
12pm

Kyle Juretus

Developing Secure Hardware: Strengthening the Root of Trust Through Obfuscation

Wednesday, January 22th
Bossone 302
1pm

Paco Sangaiah

SnackNoC: Processing in the Communication Layer

Wednesday, January 29th
Bossone 302
1pm

Scott Lerner

Optimization of On-chip Clock Delivery

Wednesday, February 5th
Bossone 302
1pm

Brad Zhou

Layout Generator for Ultra Low Power CMOS Adiabatic Circuits

Wednesday, February 12th
Bossone 302
1pm

Vaibhav Rao

Security Vulnerabilities of Obfuscated Analog Circuits

Wednesday, February 19th
Bossone 302
1pm

Jeff Wu and Pratik Shrestha

Machine Learning in Analog EDA && State of the Art Logic Encryption

Wednesday, February 26th
Bossone 302
1pm

Saran Phatharodom

Expected SAT-attack Resilience of Out-of-Cone Logic Locked Circuits

Wednesday, March 4th
Bossone 302
1pm

Ziyi Chen

Hardware Security on Mixed-Signal Circuits

Wednesday, March 11th
Bossone 302
1pm

Shazzad Hossain

Power Delivery for Sub-threshold Computing

Wednesday, March 18th
Bossone 302
1pm

FOR MORE INFO VISIT:
[DREXEL.EDU/ECE/NEWS/CALENDAR](https://drexel.edu/ece/news/calendar)