

# DREXEL COMPUTER ENGINEERING GRADUATE SYMPOSIUM

UPDATED

Breaking Down Software for Emerging Hardware

MIKE LUI | FRI JAN. 8 | BOSSONE 303 | 1PM

Hardware Security for the Internet of Things

KYLE JURETUS | FRI JAN. 15 | BOSSONE 302 | 1PM

Near-Threshold Circuit Design for Low Power Computation

SHAZZAD HOSSAIN | FRI JAN. 29 | BOSSONE 303 | 11AM

Fast Multicore Simulation & Performance Analysis of HPC Applications with SynchroTrace

PACO SANGAIAH | FRI FEB. 5 | BOSSONE 302 | 1PM

Workload-aware EDA

SCOTT LERNER | FRI FEB. 12 | BOSSONE 302 | 1PM

Logic Computation using Anomalous Capacitance Triggered by Light

RAGH KUTTAPPA | FRI FEB. 19 | BOSSONE 302 | 1PM

Multi-Component Performance and Energy Model

RIZWANA BEGUM | FRI FEB. 19 | BOSSONE 302 | 1:30PM

Wireless RAM: The Future of Architecture

VASIL PANO | FRI MAR. 4 | BOSSONE 302 | 1PM

Energy Efficient On-Chip Power Delivery for Many-Core Systems

DIVYA PATHAK | FRI MAR. 11 | BOSSONE 302 | 1PM

Charge Recovery Logic

LEO FILIPPINI | FRI MAR. 18 | BOSSONE 302 | 1PM